

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A separation adjustment circuit for adjusting an intensity ratio between a sum signal and a difference signal in a stereo composite signal and for increasing a separation degree between a stereo right signal and a stereo left signal, comprising:

a sum signal retrieving unit retrieving a sum signal from the composite signal;
a difference signal retrieving unit retrieving a difference signal from the stereo composite signal;

a mixing unit mixing the sum signal and the difference signal, thereby obtaining a stereo right signal and stereo left signal;

a first adjustment unit adjusting a current amount that flows in the sum signal retrieving unit or the difference signal retrieving unit and adjusting an intensity of the sum signal or an intensity of the difference signal; ~~and~~

a generation unit generating a control signal for controlling an adjustment operation of the first adjustment unit;

an output stage;

a resistance that is connected to the output stage; and

a second adjustment unit being connected to the resistance in parallel and adjusting a current amount flowing in the resistance, wherein the second adjustment unit adjusts a current amount that flows in the resistance based on a current amount adjusted by the first adjustment unit.

2. (Original) The separation adjustment circuit according to claim 1; wherein the first adjustment unit comprises a plurality of transistors and a selection unit selecting the plurality of transistors based on the control signal, and it adjusts an intensity of the sum signal or an intensity of the difference signal based on a total current amount of the transistors selected by the selection unit.

3. (Canceled)

4. (Original) The separation adjustment circuit according to claim 1, wherein the control signal is generated based on a separation degree between a stereo right signal and a stereo left signal that are outputted from the separation adjustment circuit.